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Lab 8: Compensator Design

In order to design a compensator for our system, we began by choosing a compensator type. For our application, a lead-lag compensator appeared to be a good choice. In order to create a lead-lag compensator, we cascaded a lead compensator with a lag compensator.

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| Screen%20Shot%202015-04-08%20at%202.42.55%20PM.png | Screen%20Shot%202015-04-08%20at%202.43.05%20PM.png |
| Figure X: Lead Compensator | Figure Y: Lag Compensator |

We chose our poles and zeroes such that the frequency response of the compensator spanned our desired bandwidth, 100Hz to 10KHz. We also aimed for attenuation of the resonant peak present in the plant transfer function. In order to be able to physically realize said compensator transfer function using a passive network, we needed to ensure that the pole to zero ratio, αLag, for the lag compensator was inversely related to the pole to zero ratio, αLead, for the lead compensator. Using these constraints, the lead and lag compensator parameters were chosen, and the compensator transfer function was obtained. An iterative process was used to find the best α for the compensator. The open loop and closed loop transfer functions were then generated.

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| Screen%20Shot%202015-04-08%20at%202.36.27%20PM.png |
| Figure X: Compensator Transfer Function |
| Screen%20Shot%202015-04-08%20at%202.36.36%20PM.png |
| Figure X: Open Loop System Transfer Function |
| Screen%20Shot%202015-04-08%20at%202.36.45%20PM.png |
| Figure X: Closed Loop System Transfer Function |

The open loop and closed loop response was examined using frequency response methods as well as root locus. The bode plots of the plant, compensator, and closed loop response were generated to ensure the compensator would have the desired effect on the system.

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| Screen%20Shot%202015-04-08%20at%203.04.35%20PM.png |
| Figure X: Compensator Bode Plot |
| Screen%20Shot%202015-04-08%20at%203.04.26%20PM.png |
| Figure X: Open Loop Bode Plot |
| Screen%20Shot%202015-04-08%20at%203.03.35%20PM.png |
| Figure Y: Closed Loop Bode Plot |

Next, the compensator transfer function was exported from MATLAB in zero-pole-gain form using MATLAB’s zpk()function.

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| Screen%20Shot%202015-04-08%20at%202.52.27%20PM.png |
| Figure X: Compensator in Zero-Pole-Gain Form |

The passive implementation of lag-lead compensators discussed on page 506 of *Control Systems Engineering, 6th Edition* by Nise was used to realize the compensator, specifically the last equation detailed in table 9.11.

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| Figure X: Physical Lag-Lead Compensator Transfer Function |

The MATLAB generated transfer function was set equal to the given physical transfer function, and each component was solved for algebraically. C1 was first chosen to be 10nF. R1 was found to be 2123Ω, R2 was found to 6443Ω, and C2 was found to be 0.185μF, which can be implemented most realistically with a 0.2μF capacitor.

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| Screen%20Shot%202015-04-08%20at%202.53.15%20PM.png |
| Figure X: LTSpice Circuit Simulation Setup |

The circuit was then simulated in LTSpice alongside the plant. A single opamp subtractor circuit is needed in order to provide the negative feedback for the system. Using LTSpice’s AC simulation, it can be seen that the compensator performs as expected and the calculated circuit elements closely enough implement the proper compensator design. Furthermore, the component values are both realistic and physically realizable. Finally, the closed loop response was generated and very well matches MATLAB’s plotted response.

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| Screen%20Shot%202015-04-08%20at%202.55.01%20PM.png |
| Figure X: Simulated Physical Compensator Response |
| Screen%20Shot%202015-04-08%20at%202.55.48%20PM.png |
| Figure X: Simulated Closed Loop System Response |

Lastly, the list of physical circuit elements was created.

1. LM741CN opamp
2. 10nF ceramic capacitor
3. 0.1uF ceramic capacitors, in parallel to create 0.2uF
4. 2.2kΩ resistor

(2) 75kΩ resistors

(2) 1kΩ resistors

(1) 6.2kΩ resistor

(1) 220Ω resistor

Though the compensator in the LTSpice simulation does not exactly match the theoretical compensator designed in MATLAB, this is to be expected. Read circuit elements not only have tolerances but also are impossible to obtain in very specific values. Rather, they much be combined to come as close as reasonably possible to the desired value. With 10% tolerance components, even more error can be expected in the physical system, which is to be built as the next part of the lab.